

## EAST Search History

| Ref # | Hits | Search Query  | DBs      | Default Operator | Plurals | Time Stamp       |
|-------|------|---|----------|------------------|---------|------------------|
| L1    | 1    | ((((reconfigur\$4 NEAR1 rout\$3) or (reconfigur\$4 NEAR1 interconnect\$3) or (reconfigur\$4 NEAR1 crossbar) or (reconfigur\$4 NEAR1 cross-bar) or (reconfigur\$4 NEAR1 matrix)) and (input/output or i/o or ((input adj1 output) NEAR1 circuit\$3)) and (multi-clock\$1 or (multiple adj1 clock\$1) or ((two or three or second) NEAR1 clock\$1))))).clm. | US-PGPUB | OR               | ON      | 2006/02/22 16:24 |
| L2    | 1    | ((((reconfigur\$4 NEAR1 rout\$3) or (reconfigur\$4 NEAR1 interconnect\$3) or (reconfigur\$4 NEAR1 crossbar) or (reconfigur\$4 NEAR1 cross-bar) or (reconfigur\$4 NEAR1 matrix)) and (input/output or i/o or (input adj1 output)) and (multi-clock\$1 or (multiple adj1 clock\$1) or ((two or three or second) NEAR1 clock\$1))))).clm.                    | US-PGPUB | OR               | ON      | 2006/02/22 16:25 |
| L3    | 2    | ((((reconfigur\$4 NEAR3 rout\$3) or (reconfigur\$4 NEAR3 interconnect\$3) or (reconfigur\$4 NEAR3 crossbar) or (reconfigur\$4 NEAR3 cross-bar) or (reconfigur\$4 NEAR3 matrix)) and (input/output or i/o or (input adj1 output)) and (multi-clock\$1 or (multiple adj1 clock\$1) or ((two or three or second) NEAR1 clock\$1))))).clm.                    | US-PGPUB | OR               | ON      | 2006/02/22 16:26 |

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| Ref # | Hits | Search Query   | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|--|---|------------------|---------|------------------|
| L1    | 26   | ((reconfigur\$4 adj1 rout\$3) or (reconfigur\$4 adj1 interconnect\$3)) and (input/output or i/o or (input adj1 output)) and (multi-clock or (multiple adj1 clock))   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/02/22 15:36 |
| L2    | 2    | ((reconfigur\$4 adj1 crossbar) or (reconfigur\$4 adj1 cross-bar) or (reconfigur\$4 adj1 matrix)) and (input/output or i/o or (input adj1 output)) and (multi-clock or (multiple adj1 clock\$1) or (plurality adj1 clock\$1))                               | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/02/22 15:33 |
| L3    | 3    | ((reconfigur\$4 adj1 crossbar) or (reconfigur\$4 adj1 cross-bar) or (reconfigur\$4 adj1 matrix)) and (input/output or i/o or (input adj1 output)) and (multi-clock or (multiple adj1 clock\$1) or (plurality adj1 clock\$1) or (select\$4 NEAR1 clock\$1)) | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/02/22 15:34 |
| L4    | 80   | ((reconfigur\$4 adj1 rout\$3) or (reconfigur\$4 adj1 interconnect\$3)) and (input/output or i/o or (input adj1 output)) and (multi-clock or (multiple adj1 clock) or ((two or three) NEAR1 clock\$1))  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/02/22 16:13 |
| L5    | 7    | 4 and ((input/output or i/o or (input adj1 output)) adj1 circuit\$3)   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/02/22 16:13 |